

FIG. 1

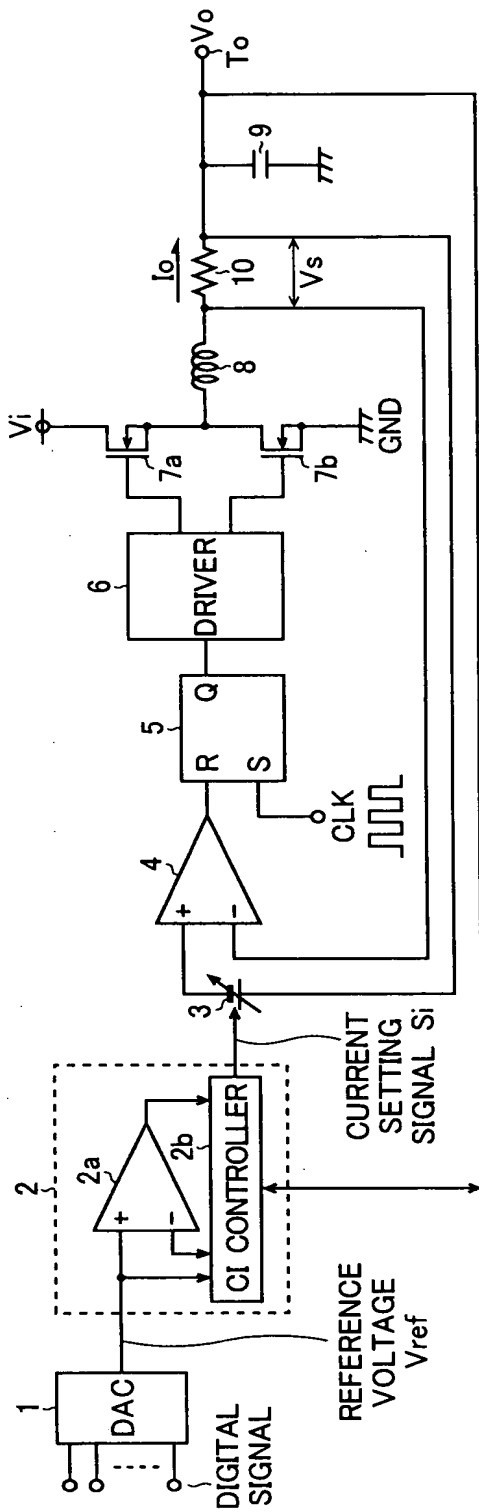


FIG. 2

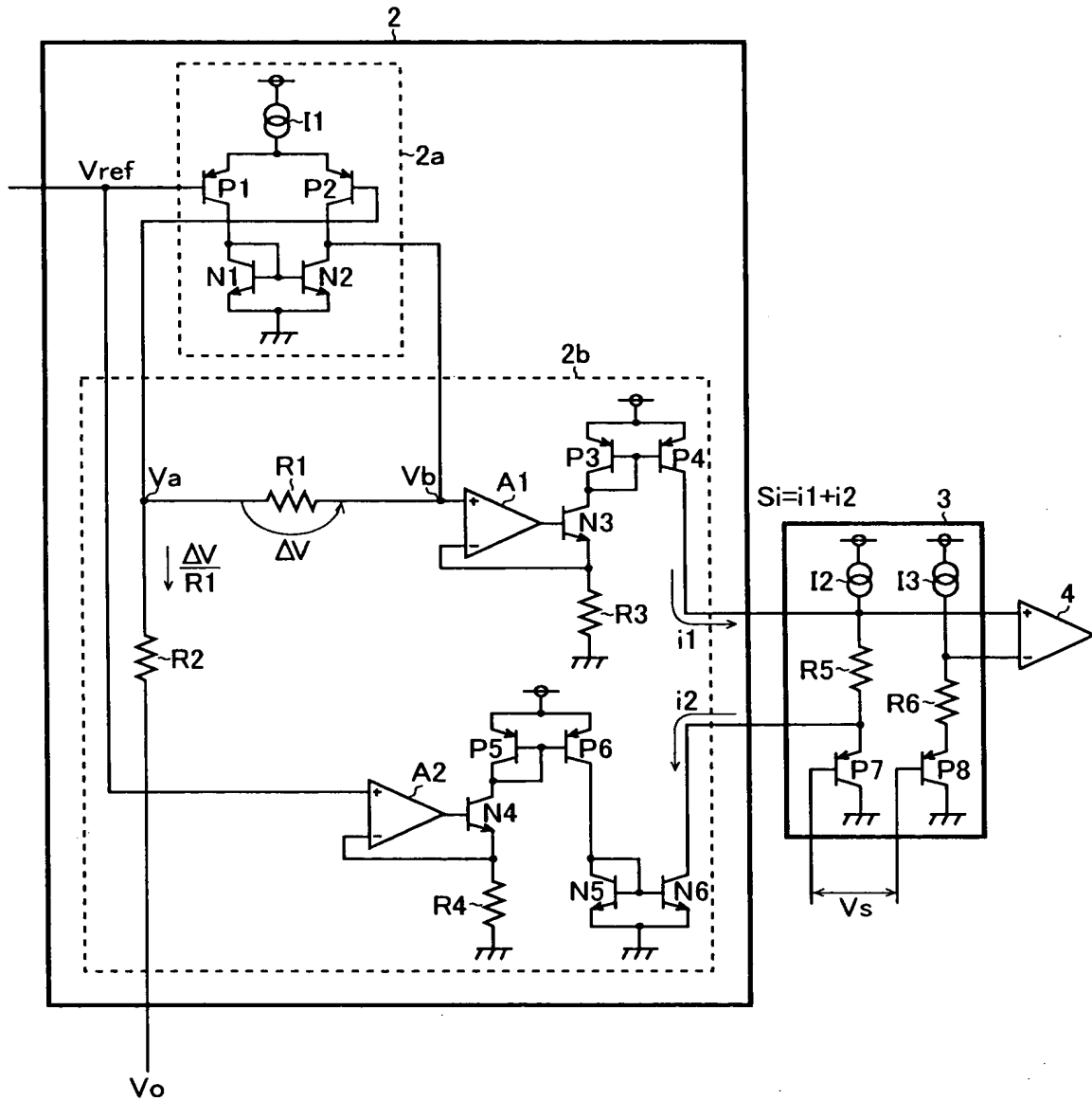


FIG. 3A

[DC CHARACTERISTIC]

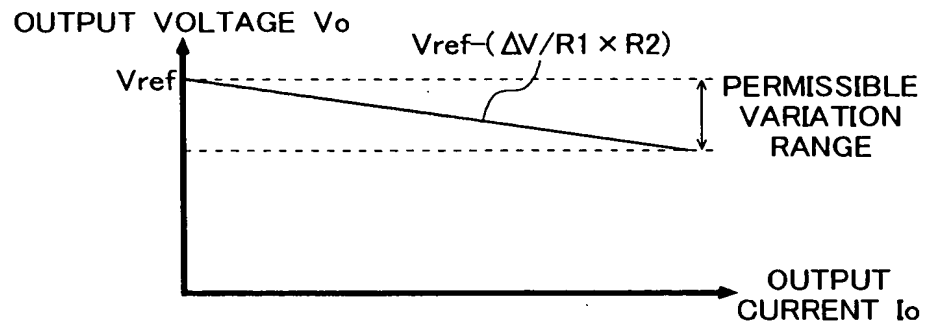


FIG. 3B

[TRANSIENT CHARACTERISTIC]

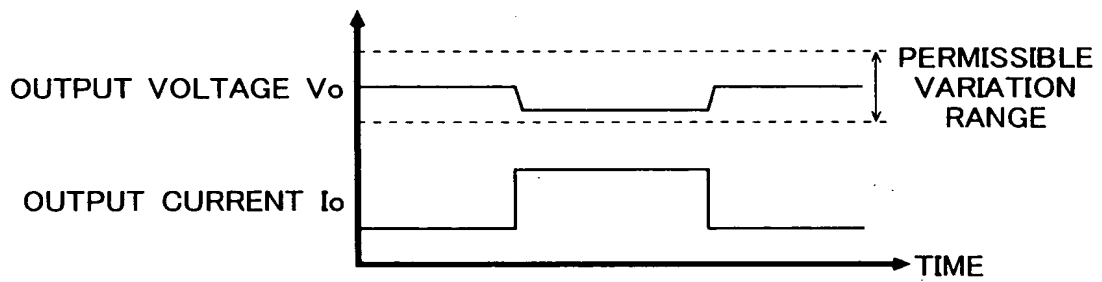


FIG. 4

CURRENT SETTING SIGNAL S_i

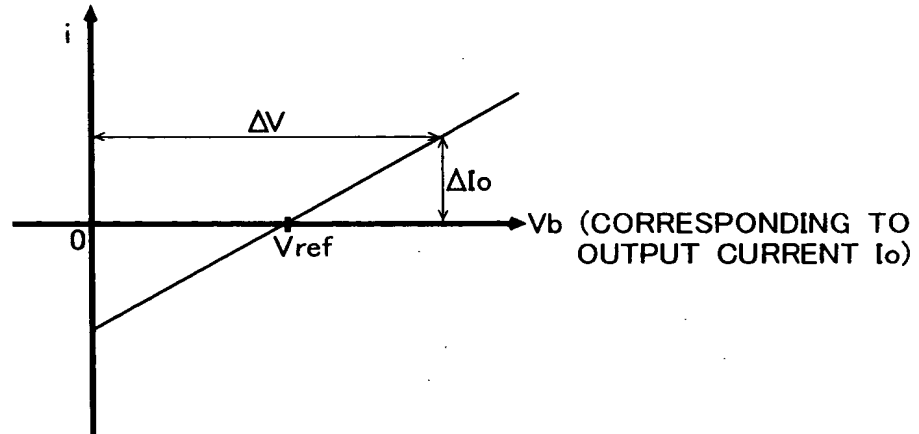


FIG. 5

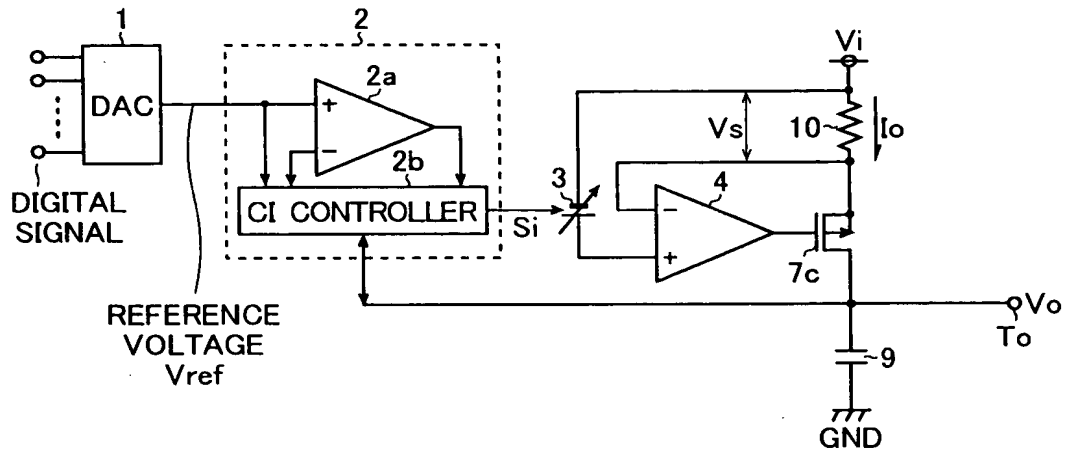


FIG. 6

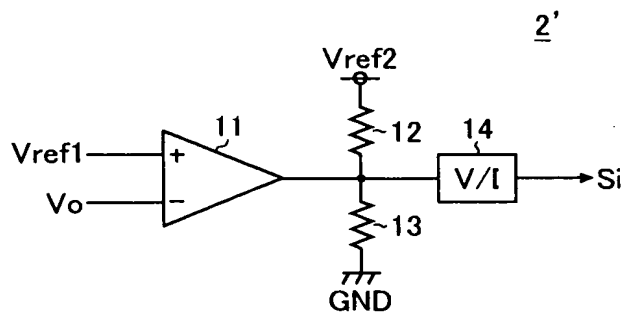


FIG. 7A
Prior Art

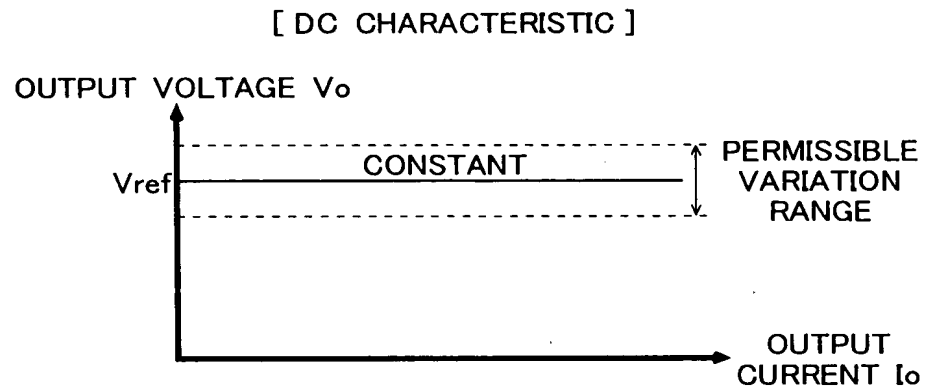


FIG. 7B
Prior Art

